

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning at page 16, line 1, with the following rewritten paragraph:

--The invention is explained in even more detail below with reference to the drawing using a specific exemplary embodiment and taking into account the considerations above. The drawing comprises the following parts, with figures 1 to 3 already having been mentioned in relation to the prior art in section 2:

figure 1 shows the basic design of a Von Neumann architecture based on the prior art,

figure 2 shows the general structure of a PLD based on the prior art,

figure 3 shows the basic design of an FSM in the form of a Mealy machine based on the prior art,

figure 4 shows an exemplary embodiment of a memory design in an architecture based on the invention,

~~figure 5 shows~~ figures 5a, 5b and 5c show the mapping of string terms onto RAM, with figure element a) illustrating partial string terms, figure element b) illustrating mapping onto a tag RAM, and figure element c) illustrating mapping onto a conventional RAM,

figure 6 shows mapping of the result from stage 1 of an architecture based on the invention onto a RAM combination in stage 2,

figure 7 shows a final architecture for the example  $[(0,1)^{12} \rightarrow (0,1)^{12} \text{ function}]$ ,

figure 8 shows an architecture based on the invention for a sequential circuit with a large combinational circuit for holding a GCA, and

figure 9 shows a reconfigurable architecture based on the invention for holding GCAs.--